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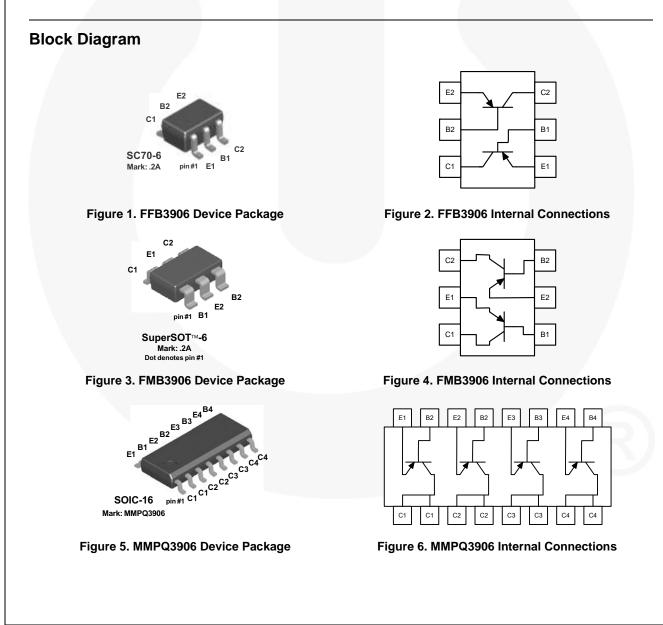
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# FFB3906 / FMB3906 / MMPQ3906 PNP Multi-Chip General-Purpose Amplifier

# Description

This device is designed for general-purpose amplifier and switching applications at collector currents of 10  $\mu$ A to 100 mA. Sourced from Process 66.



December 2013

# **Ordering Information**

Part Number	Top Mark	Package	Packing Method
FFB3906	.2A	SC70 6L	Tape and Reel
FMB3906	.2A	SSOT 6L	Tape and Reel
MMPQ3906	MMPQ3906	SOIC 16L	Tape and Reel

# Absolute Maximum Ratings<sup>(1)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	-40	V
V <sub>CBO</sub>	Collector-Base Voltage	-40	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
۱ <sub>C</sub>	Collector Current - Continuous	-200	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

### Note:

1. These ratings are based on a maximum junction temperature of 150°C. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty cycle operations.

## Thermal Characteristics<sup>(2)</sup>

Values are at T<sub>A</sub> = 25°C unless otherwise noted.

Symbol	Parameter	Max.			Unit
Symbol	Faiameter	FFB3906		MMPQ3906	Onit
р	Total Device Dissipation3007001,000				mW
P <sub>D</sub> Derate Above 25°C		2.4	5.6	8.0	mW/°C
	Thermal Resistance, Junction to Ambient	415	180		°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Effective 4 Die			125	°C/W
	Thermal Resistance, Junction to Ambient, Each Die			240	°C/W

#### Note:

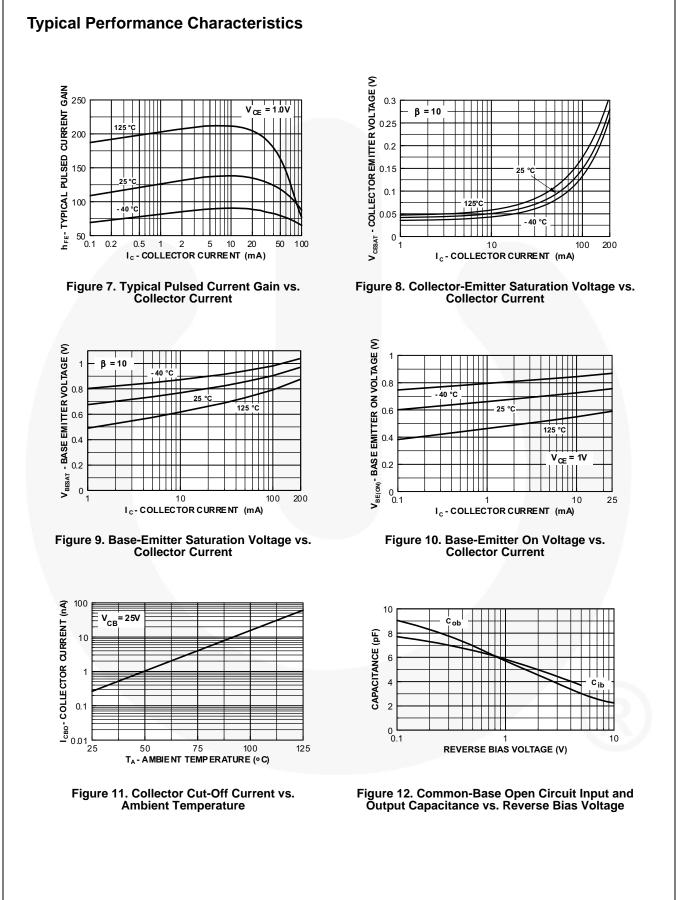
2. PCB size: FR-4 76 x 114 x 0.6T mm<sup>3</sup> (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Values are a	at T <sub>A</sub> = 25°C unless c	otherwise noted.					
Symbol	Para	meter	Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics						
V <sub>(BR)CEO</sub>	Collector-Emitter Br	eakdown Voltage <sup>(3)</sup>	I <sub>C</sub> = -1.0 mA, I <sub>B</sub> = 0	-40			V
V <sub>(BR)CBO</sub>	Collector-Base Brea	akdown Voltage	I <sub>C</sub> = -10 μA, I <sub>E</sub> = 0	-40			V
V <sub>(BR)EBO</sub>	Emitter-Base Break	down Voltage	I <sub>E</sub> = -10 μA, I <sub>C</sub> = 0	-5.0			V
I <sub>BL</sub>	Base Cut-Off Curren	nt	V <sub>CE</sub> = -30 V, V <sub>BE</sub> = 3.0 V			-50	nA
I <sub>CEX</sub>	Collector Cut-Off Cu	urrent	V <sub>CE</sub> = -30 V, V <sub>BE</sub> = 3.0 V			-50	nA
On Charac	teristics						
		FFB3906, FMB3906	1 = 0.1  m (1 - 1.0)/	60			
		MMPQ3906	I <sub>C</sub> = -0.1 mA, V <sub>CE</sub> = -1.0 V	40			
(a) MMPQ3906	FFB3906, FMB3906	1 - 10 - 10	80				
	DC Current Gain <sup>(3)</sup>	MMPQ3906	I <sub>C</sub> = -1.0 mA, V <sub>CE</sub> = -1.0 V	60			
h <sub>FE</sub>	DC Current Gainer	FFB3906, FMB3906	1 = 10 m (1 - 10)/	60 300			
		MMPQ3906	$I_{\rm C}$ = 10 mA, $V_{\rm CE}$ = -1.0 V	75			
		All Devices	I <sub>C</sub> = -50 mA, V <sub>CE</sub> = -1.0 V	60			
		All Devices	I <sub>C</sub> = -100 mA, V <sub>CE</sub> = -1.0 V	30			
V	Collector Emitter Sc	turation Voltage	I <sub>C</sub> = -10 mA, I <sub>B</sub> = -1.0 mA			-0.25	V
V <sub>CE(sat)</sub>	Collector-Emitter Sa	aturation voltage	I <sub>C</sub> = -50 mA, I <sub>B</sub> = -5.0 mA			-0.40	v
V	Page Emitter Setur		I <sub>C</sub> = -10 mA, I <sub>B</sub> = -1.0 mA	-0.65		-0.85	V
V <sub>BE(sat)</sub>	Dase-Emiller Salura	$I_{C} = -50 \text{ mA}, I_{B} = -5.0 \text{ mA}$		-0.95	v		
Small-Sigr	nal Characteristics (	(MMPQ3906 Only)					
f <sub>T</sub>	Current Gain-Bandv	width Product	I <sub>C</sub> = -10 mA, V <sub>CE</sub> = -20 V, f = 100 MHz		200		MHz
C <sub>ob</sub>	Output Capacitance		V <sub>CB</sub> = -5.0 V, I <sub>E</sub> = 0, f = 140 kHz		4.5		pF
C <sub>ib</sub>	Input Capacitance		V <sub>EB</sub> = -0.5 V, I <sub>C</sub> = 0, f = 140 kHz		10		pF

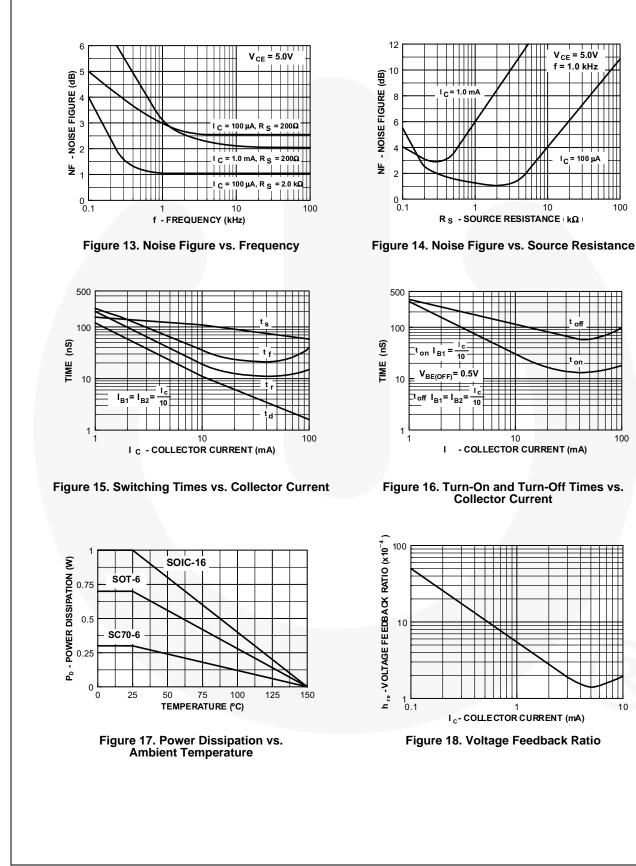
### Note:

3. Pulse test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2.0%.

**Electrical Characteristics** 



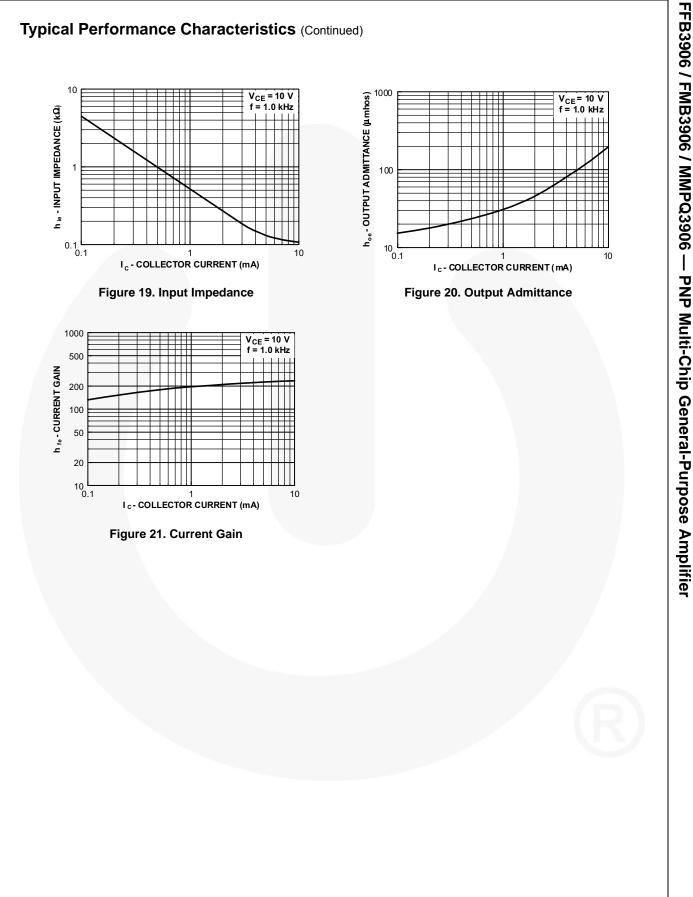
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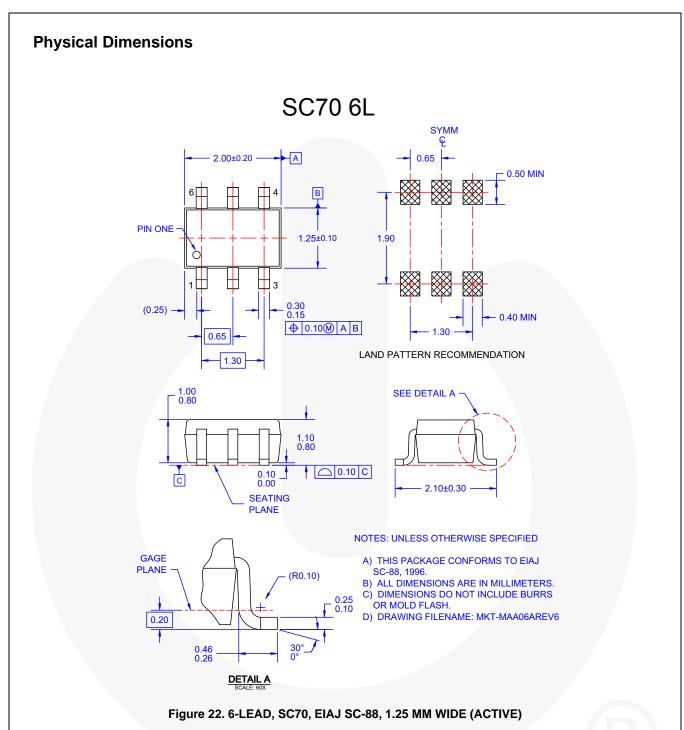


Typical Performance Characteristics (Continued)



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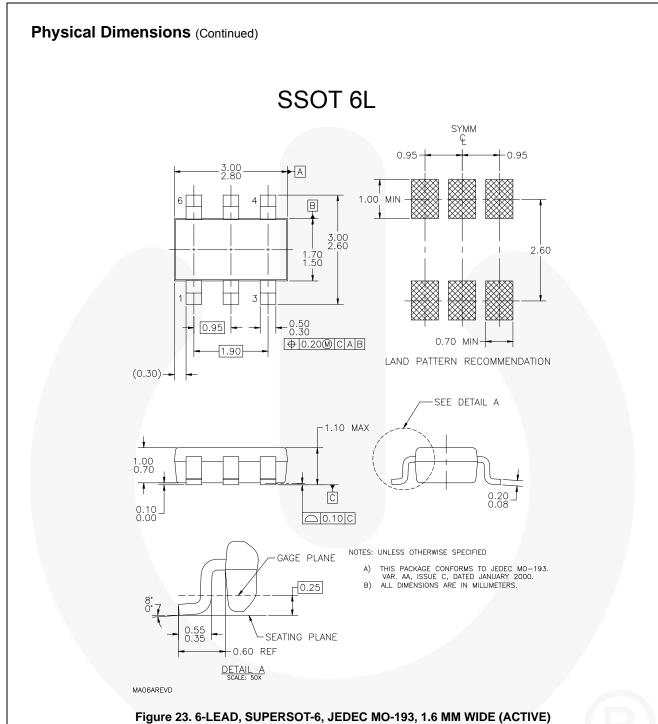


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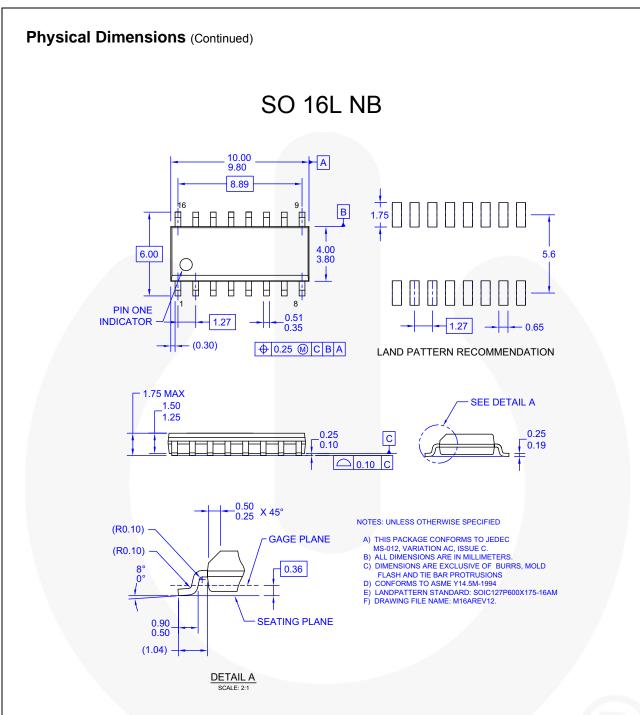
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## Figure 24. 16-LEAD, SOIC, JEDEC MS-012, 0.150 inch, NARROW BODY (ACTIVE)

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