SOT-23 (TO-236)

Vishay Siliconix

# Automotive P-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	-60				
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS}$ = -10 V	0.170				
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = -4.5 \text{ V}$	0.230				
I <sub>D</sub> (A)	-2.9				
Configuration	Single				

# Single

P-Channel MOSFET

### **FEATURES**

- TrenchFET® power MOSFET
- Typical ESD protection: 800 V
- AEC-Q101 qualified
- 100 % R<sub>a</sub> and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





Marking Code: 9Cxxx

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ORDERING INFORMATION				
Package	SOT-23			
Lead (Pb)-free and Halogen-free	SQ2361AEES-T1-GE3			

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage		V <sub>DS</sub>	-60	.,		
Gate-Source Voltage		V <sub>GS</sub>	± 20	V		
Continuous Drain Current	T <sub>C</sub> = 25 °C	,	-2.8			
Continuous Drain Current	T <sub>C</sub> = 125 °C	l <sub>D</sub>	-1.6			
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	-2.5	Α		
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	-11			
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	-13			
Single Pulse Avalanche Energy	L = 0.1 IIIIA	E <sub>AS</sub>	8.4	mJ		
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	- P <sub>D</sub>	2	W		
	T <sub>C</sub> = 125 °C		0.67	VV		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C		

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount b	R <sub>thJA</sub>	175	°C/W
Junction-to-Foot (Drain)		$R_{thJF}$	75	C/VV

### Notes

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. When mounted on 1" square PCB (FR4 material).



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PARAMETER	SYMBOL	TES	TEST CONDITIONS		TYP.	MAX.	UNIT	
Static					l	l		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> =	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		-	-	V	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	V <sub>GS</sub> , I <sub>D</sub> = -250 μA	-1.5	-	-2.5	V	
Gate-Source Leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 30	mA	
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$		-	± 2		
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = -60 V	-	-	-1	]	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = -60 V, T <sub>J</sub> = 125 °C	-	-	-50	μA	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = -60 V, T <sub>J</sub> = 175 °C	-	-	-150	1	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = -10 V	V <sub>DS</sub> ≤ -5 V	-10	-	-	Α	
		V <sub>GS</sub> = -10 V	I <sub>D</sub> = -2.4 A	-	0.130	0.170		
Duain Causes On State Besistance 3	В	V <sub>GS</sub> = -10 V	I <sub>D</sub> = -2.4 A, T <sub>J</sub> = 125 °C	-	-	0.300		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10 V	I <sub>D</sub> = -2.4 A, T <sub>J</sub> = 175 °C	-	-	0.315	Ω	
		V <sub>GS</sub> = -4.5 V	I <sub>D</sub> = -1.8 A	-	0.180	0.230		
Forward Transconductance b	9 <sub>fs</sub>	V <sub>DS</sub>	= -10 V, I <sub>D</sub> = -2 A	-	5	-	S	
Dynamic <sup>b</sup>								
Input Capacitance	C <sub>iss</sub>			-	415	620		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = -30 V, f = 1 MHz	-	55	80	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	32	45		
Total Gate Charge c	$Q_g$			-	10	15		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = -10 V	$V_{DS} = -30 \text{ V}, I_{D} = -6 \text{ A}$	-	1.5	=	nC	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			-	5	-		
Gate Resistance	$R_g$	f = 1 MHz		3.2	4.3	5.4	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	9	12		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = -30 V, $R_L$ = 20 $\Omega$ $I_D \cong$ -1.5 A, $V_{GEN}$ = -10 V, $R_g$ = 1 $\Omega$		-	9	12	- ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	24	30		
Fall Time <sup>c</sup>	t <sub>f</sub>			-	4	6		
Source-Drain Diode Ratings and Characteristics <sup>b</sup>								
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	-13	Α	
Forward Voltage	$V_{SD}$	I <sub>F</sub> = -1.5 A, V <sub>GS</sub> = 0 V		-	-0.8	-1.2	V	

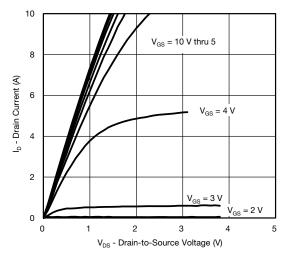
### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

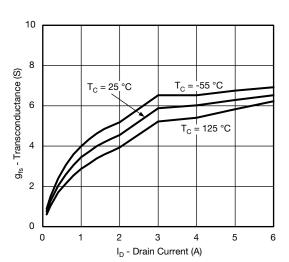
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



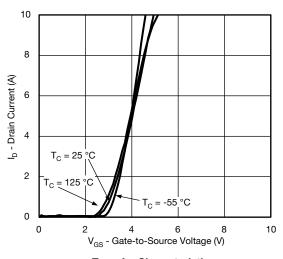
# TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



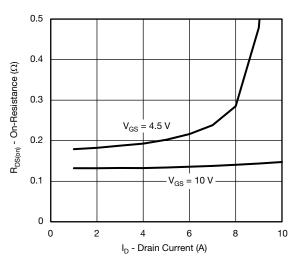
### **Output Characteristics**



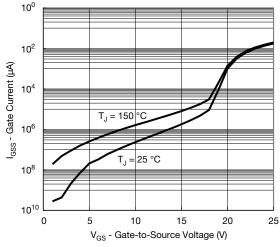
Transconductance



**Transfer Characteristics** 



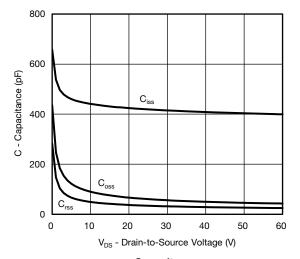
**On-Resistance vs. Drain Current** 



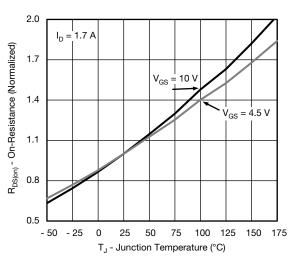
**Gate Current vs. Gate-Source Voltage** 



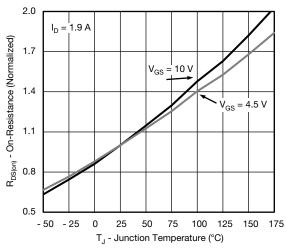
# **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



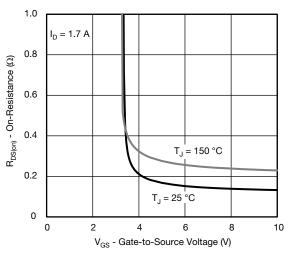
### Capacitance



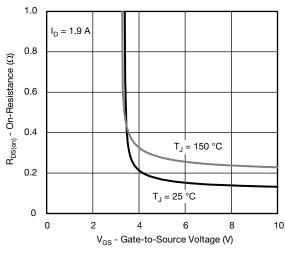
On-Resistance vs. Junction Temperature



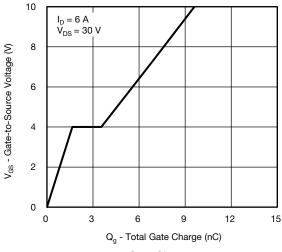
On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-Source Voltage



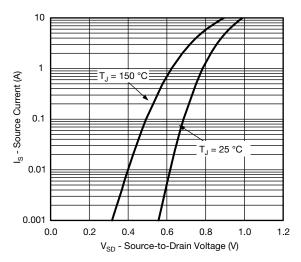
On-Resistance vs. Gate-Source Voltage



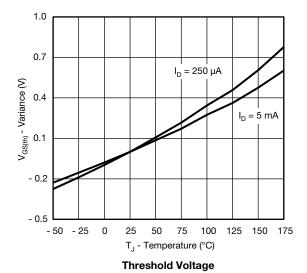
For technical questions, contact: automostech

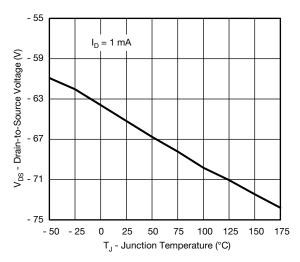


# TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)

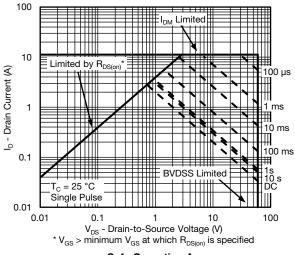


### Source-Drain Diode Forward Voltage





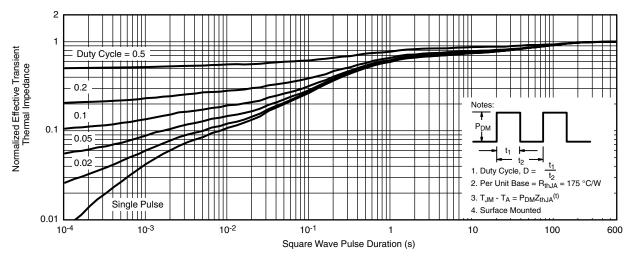
**Drain Source Breakdown vs. Junction Temperature** 



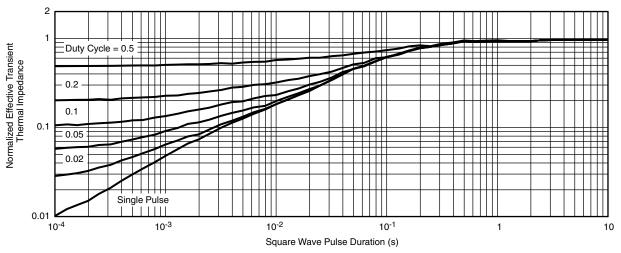
Safe Operating Area



# THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



### Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

### Note

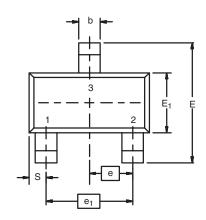
- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

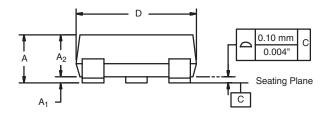
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

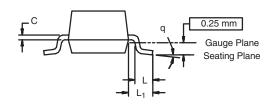
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# SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
FCN: S-03946-Rev K 09-	lul-01	•			

ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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