SQ4917EY

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Vishay Siliconix

Automotive Dual P-Channel 60 V (D-S) 175 °C MOSFET



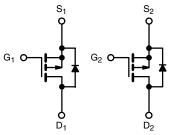
PRODUCT SUMMARY				
V _{DS} (V)	-60			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -10 \text{ V}$	0.0480			
$R_{DS(on)}$ (Ω) at V_{GS} = -4.5 V	0.0612			
I _D (A) per leg	-8			
Configuration	Dual			

FEATURES

- TrenchFET[®] power MOSFET
- AEC-Q101 qualified ^c
- 100 % $\rm R_g$ and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



RoHS COMPLIANT HALOGEN FREE



P-Channel MOSFET P-Channel MOSFET

ORDERING INFORMATION

Package	SO-8			
Lead (Pb)-free and halogen-free	SQ4917EY (for detailed order number please see www.vishav.com/doc?79771)			

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	-60		
Gate-source voltage		V _{GS}	± 20	V	
Continuous drain current	T _C = 25 °C	- I _D -	-8		
Continuous drain current	T _C = 125 °C		-4.75		
Continuous source current (diode conduction)		I _S	-4.5	А	
Pulsed drain current ^a		I _{DM}	-32		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	-22.4		
Single pulse avalanche energy		E _{AS}	25	mJ	
Movimum power discipation a	T _C = 25 °C	D	5	w	
Maximum power dissipation ^a	T _C = 125 °C	P _D	1.67	~~~	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount ^b	R _{thJA}	110	°C/W	
Junction-to-foot (drain)		R _{thJF}	30	0/10	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. When mounted on 1" square PCB (FR-4 material)

c. Parametric verification ongoing

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PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT	
Static		-			•	•	
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = -250 \mu\text{A}$		-60	-	-	v
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} =$	V_{GS} , I_D = -250 μ A	-1.5	-2.0	-2.5	v
Gate-source leakage	I _{GSS}	V _{DS} =	0 V, $V_{GS} = \pm 20 V$	-	-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = -60 V	-	-	-1	
Zero gate voltage drain current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = -60 V, T _J = 125 °C	-	-	-50	μA
		$V_{GS} = 0 V$	V _{DS} = -60 V, T _J = 175 °C	-	-	-150	1
On-state drain current ^a	I _{D(on)}	$V_{GS} = -10 V$	$V_{DS} \le -5 V$	-30	-	-	Α
		$V_{GS} = -10 V$	I _D = -4.3 A	-	0.0400	0.0480	Ω
Drain acurac an atota registance à	Passa	V _{GS} = -10 V	I _D = -4.3 A, T _J = 125 °C	-	-	0.0780	
Drain-source on-state resistance ^a	R _{DS(on)}	$V_{GS} = -10 V$	$I_D = -4.3 \text{ A}, \text{ T}_J = 175 \ ^\circ\text{C}$	-	-	0.0960	
		$V_{GS} = -4.5 V$	I _D = -3.8 A	-	0.0510	0.0612	
Forward transconductance b	9 _{fs}	V _{DS} =	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -4.3 \text{ A}$		13	-	S
Dynamic ^b							
Input capacitance	C _{iss}		V _{GS} = 0 V V _{DS} = -30 V, f = 1 MHz	-	1530	1910	pF
Output capacitance	C _{oss}	$V_{GS} = 0 V$		-	334	417	
Reverse transfer capacitance	C _{rss}			-	114	142	
Total gate charge ^c	Qg			-	43.4	65	
Gate-source charge ^c	Q _{gs}	V _{GS} = -10 V	V_{DS} = -30 V, I_D = -5 A	-	4.7	-	nC
Gate-drain charge ^c	Q _{gd}			-	9	-	
Gate resistance	R _g		f = 1 MHz		2.5	4	Ω
Turn-on delay time ^c	t _{d(on)}			-	11	17	
Rise time ^c	tr		$V_{DD} = -30 \text{ V}, \text{ R}_1 = 8.8 \Omega$		11	17	
Turn-off delay time ^c	t _{d(off)}	$I_D \cong -5 \text{ A}, V_{GEN} = -10 \text{ V}, \text{ R}_g = 1 \Omega$		-	35	52	ns
Fall time ^c	t _f			-	6	9	
Source-Drain Diode Ratings and Char	acteristics ^b	<u> </u>					
Pulsed current ^a	I _{SM}			-	-	-32	Α
Forward voltage	V _{SD}	I _F = -2.8 A, V _{GS} = 0 V		-	-0.8	-1.2	V

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

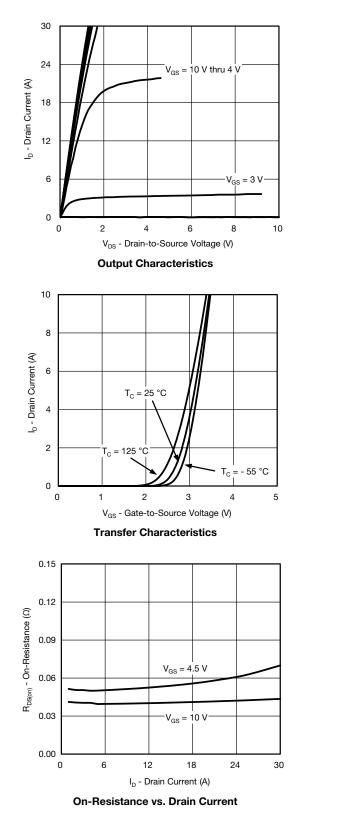
c. Independent of operating temperature

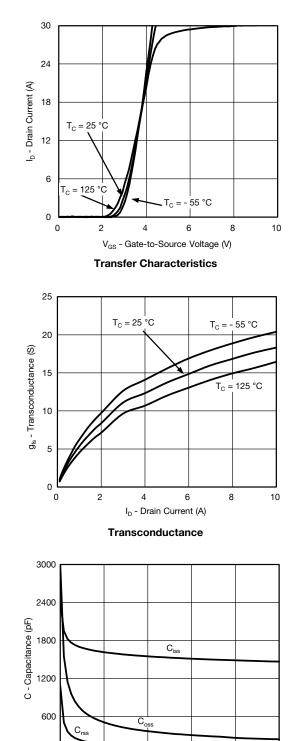
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)





V_{DS} - Drain-to-Source Voltage (V) Capacitance

36

24

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3

0

0

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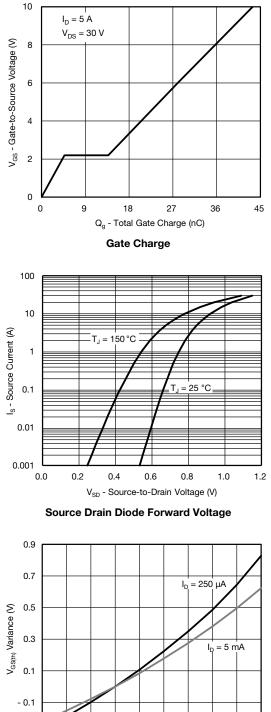
48

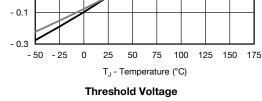
60

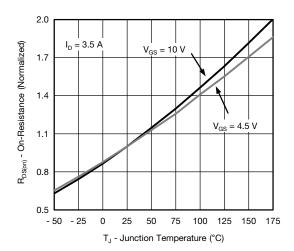
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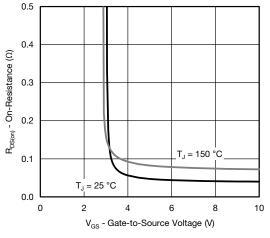
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



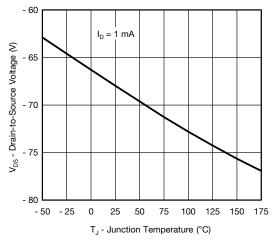




On-Resistance vs. Junction Temperature







Drain Source Breakdown vs. Junction Temperature

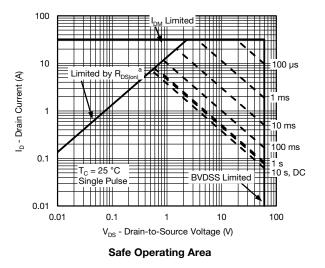
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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)

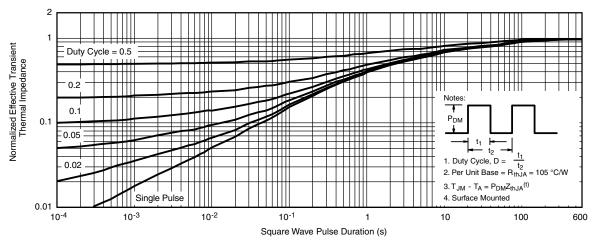


Note

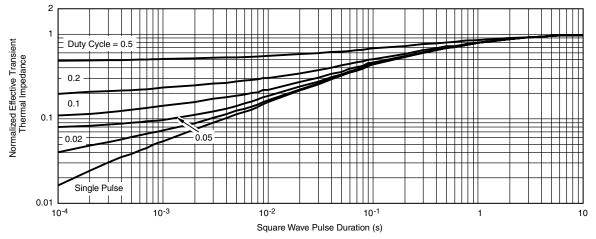
a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62785.

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Package Information

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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIMETERS		INC	HES	
DIM	Min	Мах	Min	Max	
A	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498					

Application Note 826

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RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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