N-Channel 60-V (D-S) MOSFET

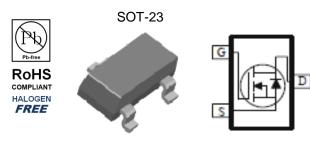
Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
Vds (V)	$r_{DS(on)}(\Omega)$	I⊳(A)	
60	3 @ V _{GS} = 10V	0.6	
	4 @ V _{GS} = 4.5V	0.5	



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)					
Parameter			Limit	Units	
Drain-Source Voltage		V _{DS}	60	V	
Gate-Source Voltage		V _{GS}	±20	v	
Continuous Drain Current ^a	T _A =25°C		0.6	A	
Continuous Drain Current	T _A =70°C	I _D	0.5		
Pulsed Drain Current ^b		I _{DM}	15		
Continuous Source Current (Diode Conduction) ^a			0.6	А	
Dower Discinction ^a	T _A =25°C	P _D	1.3	W	
Power Dissipation ^a	T _A =70°C	U 'D	0.8		
Operating Junction and Storage Temperature Range			-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter			Maximum	Units	
Maximum Junction-to-Ambient ^a	t <= 10 sec	R _{θJA}	100	°C/W	
	Steady State		166		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

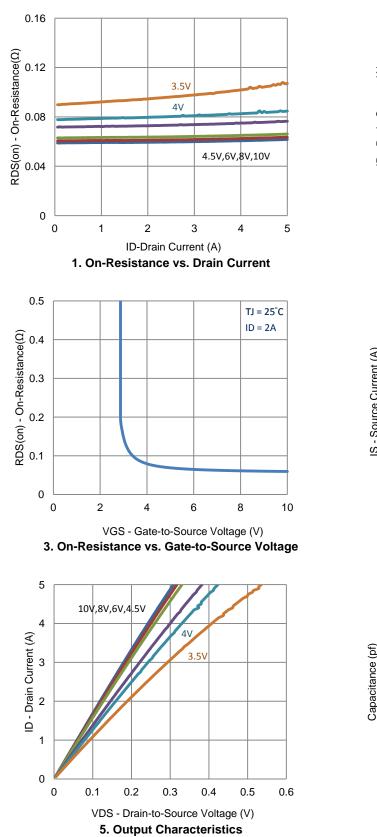
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
	DSS	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	0.9			Α	
Drain-Source On-Resistance ^a	r	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 0.6 \text{ A}$			3	Ω	
	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 0.4 \text{ A}$			4		
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 0.6 \text{ A}$		2		S	
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 0.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.73		V	
	Dynamic ^b						
Total Gate Charge	Qg	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V},$ $I_{D} = 0.6 \text{ A}$		4		nC	
Gate-Source Charge	Q _{gs}			1.5			
Gate-Drain Charge	Q _{gd}			1.4			
Turn-On Delay Time	t _{d(on)}	V = 30 V R = 50 0		2			
Rise Time	t _r	$V_{DS} = 30 \text{ V}, \text{ R}_{L} = 50 \Omega,$ $I_{D} = 0.6 \text{ A},$ $V_{GEN} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		4		ns	
Turn-Off Delay Time	t _{d(off)}			19			
Fall Time	t _f			5			
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 Mhz		474			
Output Capacitance	C _{oss}			36		pF	
Reverse Transfer Capacitance	C _{rss}			26			

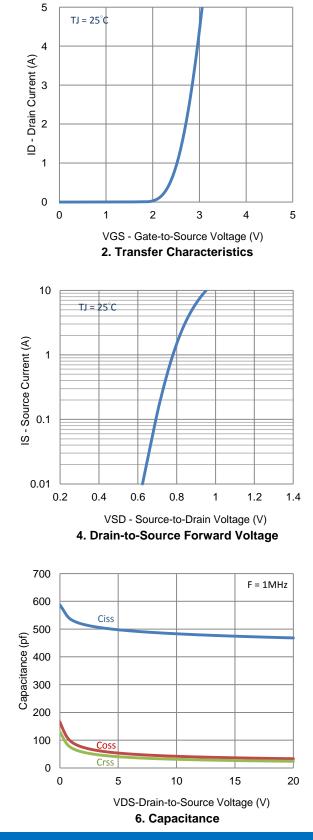
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

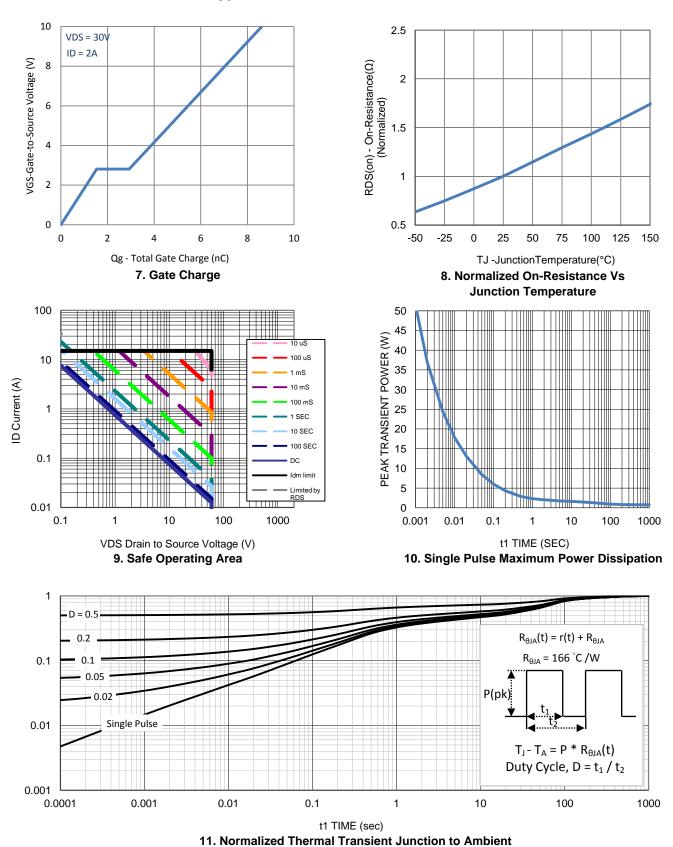
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Typical Electrical Characteristics

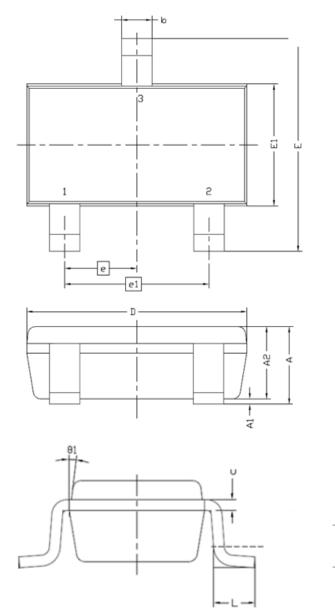


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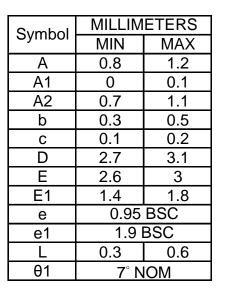


Typical Electrical Characteristics

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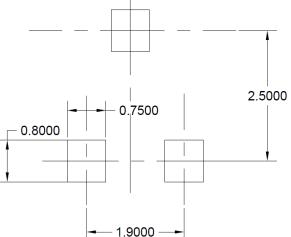


Package Information



Recommended Pad Layout

Note: Drain opening is recommended to be solder mask defined in a copper fill to provide improved thermal performance



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