

DM54ALS245A/DM74ALS245A Octal TRI-STATE® Bus Transceivers

General Description

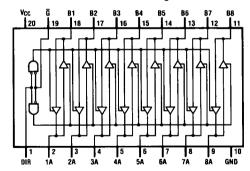
This advanced low power Schottky device contains 8 pairs of TRI-STATE logic elements configured as octal bus transceivers. These circuits are designed for use in memory, microprocessor systems and in asynchronous bidirectional data buses. Two way communication between buses is controlled by the (DIR) input. Data transmits either from the A bus to the B bus or from the B bus to the A bus. Both the driver and receiver outputs can be disabled via the (\overline{G}) enable input which causes outputs to enter the high impedance mode so that the buses are effectively isolated.

Features

- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Non-inverting logic output
- Glitch free bus during power up and down
- TRI-STATE outputs independently controlled on A and B buses
- \blacksquare Low output impedance to drive terminated transmission lines to 133 Ω
- Switching response specified into 500Ω/50 pF
- Specified to interface with CMOS at V_{OH} = V_{CC} 2V
- PNP inputs to reduce input loading
- Switching specifications guaranteed over full temperature and V_{CC} range

Connection Diagram

Dual-In-Line Package



Order Number DM54ALS245AJ or DM74ALS245AWM, N See NS Package Number J20A, M20B or N20A TL/F/6213-1

Function Table

Control Inputs		Operation
G	DIR	
L	L	B Data to A Bus
L	н	A Data to B Bus
н	×	Hi-Z

H = High Logic Level

L = Low Logic Level

X = Either High or Low Logic Level

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage
Control Inputs 7V
I/O Ports 5.5V

Operating Free Air Temperature Range

DM54ALS -55°C to +125°C DM74ALS 0°C to +70°C

Storage Temperature Range

-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54ALS245A			D	Units		
	rai ailletei	Min	Тур	Max	Min	Тур	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	4.5	5	5.5	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.7			0.8	V
Юн	High Level Output Current			-12			- 15	mA
loL	Low Level Output Current			12			24	mA
T _A	Operating Free Air Temperature	-55		125	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range. All typical values are measured at V_{CC} = 5V, T_A = 25°C.

Symbol	Parameter		Conditions		Min	Тур	Max	Units	
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5V$, $I_{IN} = -18 \text{ mA}$					-1.5	٧	
V _{OH}	OH High Level Output Voltage		$V_{CC} = 4.5V, I_{OH} = -3 \text{ mA}$			3.2		V	
		V _{CC} = 4.5V, I _{OH} = Max			2	2.3		V	
		$I_{OH} = -0.4 \text{ mA}, V_{CC} = 4.5 \text{V to } 5.5 \text{V}$			V _{CC} - 2			٧	
V _{OL} Low Level Output Voltage	V _{CC} = 4.5V	54/74ALS I _{OL} = 12 mA			0.25	0.4	٧		
			74ALS I _{OL} = 24 mA			0.35	0.5	V	
I _I Input Current at Max Input Voltage	Input Current at Max	V _{CC} = 5.5V	$V_{IN} = 7V$	Control Inputs			0.1	mA	
	Input Voltage		$V_{IN} = 5.5V$	A or B Ports			0.1		
Iн	High Level Input Current	$V_{CC} = 5.5V, V_{IN} = 2.7V$					20	μΑ	
1 _{IL}	Low Level Input Current	$V_{CC} = 5.5V, V_{IN} = 0.4V$					-0.1	mA	
Ю	Output Drive Current	V _{CC} = 5.5V, V _{OUT} = 2.25V			-30		-112	mA	
	54ALS254A Supply Current	$V_{CC} = 5.5V$	Outputs High			30	48	mA	
			Outputs Low			38	60	mA	
			TRI-STATE			38	63	mA	
ICC 74ALS254A Supply	74ALS254A Supply Current	S254A Supply Current V _{CC} = 5.5V	Outputs High			30	45	mA	
			Outputs Low			36	55	mA	
				•		38	58	mA	

Switching Characteristics over recommended operating free air temperature range (Notes 1 and 2)

Symbol	Parameter	Circuit	DM54ALS245A		DM74ALS245A		Units
	Farameter	Configuration	Min	Max	Min	Max]
t _{PLH}	Propagation Delay Time High-to-Low Level Output		1	19	3	10	ns
t _{PHL}	Propagation Delay Time High-to-Low Level Output	IN A OR B B OR A OUT	1	14	3	10	ns
tpzL	Output Enable Time to Low Level		2	29	5	20	ns
t _{PZH}	Output Enable Time to High Level	A OR B	2	30	5	20	ns
t _{PLZ}	Output Disable Time from Low Level		2	30	4	15	ns
t _{PHZ}	Output Disable Time from High Level		2	14	2	10	ns

Note 1: See Section 1 for test waveforms and output load.

Note 2: Switching characteristic conditions are $V_{CC}=4.5V$ to 5.5V, $R_L=500\Omega$, $C_L=50$ pF.