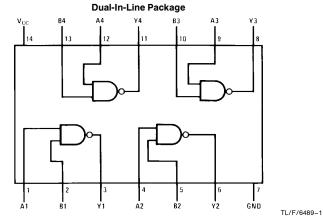
DM54S00/DM74S00 Quad 2-Input NAND Gates

General Description

This device contains four independent gates each of which performs the logic NAND function.

Connection Diagram



Order Number DM54S00J, DM54S00W, DM74S00M or DM74S00N See NS Package Number J14A, M14A, N14A or W14B

Function Table

$$Y = \overline{AB}$$

Inp	uts	Output		
Α	В	Υ		
L	L	Н		
L	Н	Н		
Н	L	Н		
Н	Н	L		

 $H \,=\, High\,\, Logic\,\, Level$

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage 5.5V
Operating Free Air Temperature Range

 DM54S
 -55°C to +125°C

 DM74S
 0°C to +70°C

 Storage Temperature Range
 -65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54S00			DM74S00			Units
	i didilictei	Min	Nom	Max	Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.8			0.8	V
loH	High Level Output Current			-1			-1	mA
l _{OL}	Low Level Output Current			20			20	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

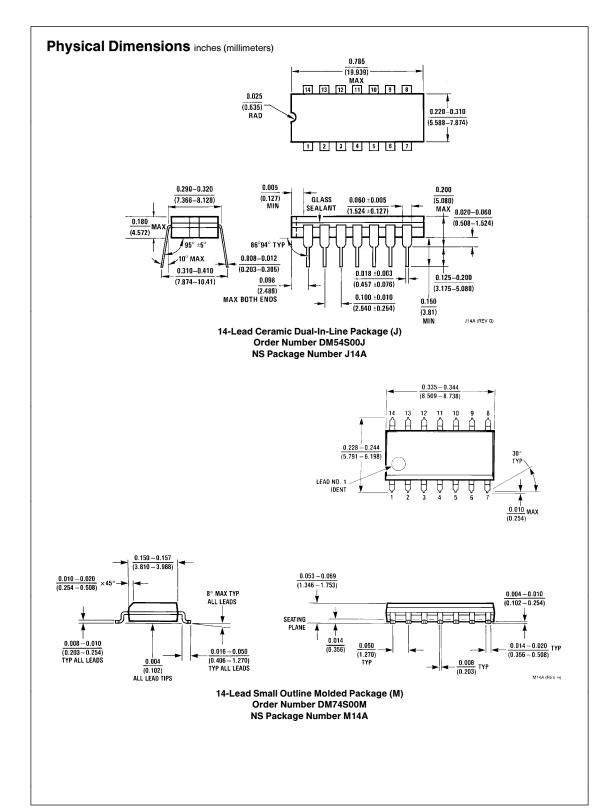
Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$				-1.2	٧
V _{OH}	V _{OH} High Level Output	V _{CC} = Min, I _{OH} = Max	DM54	2.5	3.4		V
	Voltage	V _{IL} = Max	DM74	2.7	3.4		
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min$				0.5	V
II	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$				50	μΑ
I _{IL}	Low Level Input Current	$V_{CC} = Max, V_I = 0.5V$				-2	mA
los	Short Circuit	V _{CC} = Max (Note 2)	DM54	-40		-100	- mA
Output Curre	Output Current		DM74	-40		-100	
Icch	Supply Current with Outputs High	V _{CC} = Max			10	16	mA
I _{CCL}	Supply Current with Outputs Low	V _{CC} = Max			20	36	mA

$\textbf{Switching Characteristics} \text{ at V}_{CC} = 5 \text{V and T}_{A} = 25 ^{\circ}\text{C (See Section 1 for Test Waveforms and Output Load)}$

Symbol	Parameter	C _L =	15 pF	C _L =	Units	
		Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time Low to High Level Output	2	4.5	2	7	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	2	5	2	8	ns

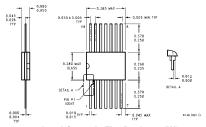
Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Note 2: Not more than one output should be shorted at a time and the duration should not exceed one second.



Physical Dimensions inches (millimeters) (Continued) 14 13 12 11 10 9 8 14 13 12 1 2 3 4 5 $\frac{0.092}{(2.337)}$ DIA $\frac{0.030}{(0.762)}$ MAX OPTION 02 0.135 ± 0.005 (3.429 ± 0.127) $\frac{0.065}{(1.651)}$ 0.060 (1.524) TYP $\frac{0.125 - 0.150}{(3.175 - 3.810)}$ 0.075 ± 0.015 (1.905 ± 0.381) 0.280 (7.112)-MIN 0.014-0.023 (0.356-0.584) TYP 0.050 ± 0.010 (1.270 - 0.254) TYP 0.325 + 0.040 - 0.015 $\begin{array}{r} -0.015 \\ \hline (8.255 + 1.016 \\ -0.381) \end{array}$

14-Lead Molded Dual-In-Line Package (N) Order Number DM74S00N NS Package Number N14A



14-Lead Ceramic Flat Package (W) Order Number DM54S00W NS Package Number W14B

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