## 82HS641A/B

#### **FEATURES**

- Address access time:
  82HS641A = 55ns max
  82HS641B = 45ns max
- Input loading: -100µA max
- One chip enable input
- On-chip address decoding
- No separate fusing pins
- Unprogrammed outputs are High level
- Fully TTL compatible
- Outputs: 3-State

#### **APPLICATIONS**

- Prototyping/volume production
- Sequential controllers
- Microprogramming
- Hardwired algorithms
- Control store

- Random logic
- Code conversion

#### DESCRIPTION

The 82HS641 is field programmable which means that custom patterns are immediately available by following the Philips Generic II fusing Procedure. The 82HS641 is supplied with all outputs at a logical High. Outputs are programmed to a logic low level at any specified address by fusing the vertical junction matrix.

This device includes on-chip address decoding with 1 chip enable input for ease of memory expansion. It features 3-State outputs for optimization of word expansion in bused applications.

#### ORDERING INFORMATION

DESCRIPTION	ORDER CODE	PACKAGE DESIGNATOR*		
24-pin Ceramic DIP (600 mil wide)	82HS641A/BJA 82HS641B/BJA	GDIP1-T24		
28-pin Ceramic LLCC	82HS641A/B3A 82HS641B/B3A	GDFP2-F28		

<sup>\*</sup> MIL-STD 1835 or Appendix A of 1995 Military Data Handbook

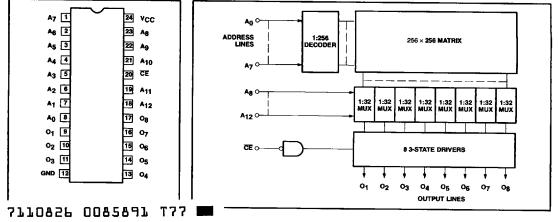
## **ABSOLUTE MAXIMUM RATINGS**

SYMBOL	PARAMETER	RATING	V <sub>DC</sub>	
Vcc	Power supply voltage	+7		
V <sub>IN</sub>	Input voltage	+5.5		
Vo	Output voltage Off-State	+5.5	V <sub>DC</sub>	
TA	Operating temperature range	-55 to +125	°C	
T <sub>STG</sub>	Storage temperature range	-65 to +150	°c	

NOTE: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operation sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### PIN CONFIGURATION

#### **BLOCK DIAGRAM**



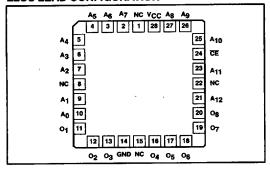
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## LLCC LEAD CONFIGURATION



## DC ELECTRICAL CHARACTERISTICS -55°C ≤ TA ≤ +125°C, 4.5V ≤ V<sub>CC</sub> ≤ 5.5V

SYMBOL	PARAMETER	TEST CONDITIONS <sup>1, 2</sup>		LIMITS		
			Min	Typ <sup>5</sup>	Max	
input volta	ge					
V <sub>IL</sub>	Low <sup>3</sup>				0.8	V
ViH	High <sup>3</sup>		2.0	l		٧
VIK	Clamp	V <sub>CC</sub> = 4.5V, I <sub>I</sub> = 18mA		-0.8	-1.2	٧
Output vol	tage					
		V <sub>CC</sub> = 4.5V, <del>CE</del> = Low		1		
$V_{OL}$	Low	I <sub>O</sub> = 16mA.	- 1	i	0.5	V
VOH	High	l <sub>O</sub> = -2mA	2.4	<u>.                                    </u>		٧
Input curre	nt					
		V <sub>CC</sub> = 5.5V				
l <sub>IL</sub>	Low	V <sub>I</sub> = 0.45V	ļ	ļ	-100	μА
I <sub>IH</sub>	High	V <sub>1</sub> = 5.5V	ł		40	μА
Output cur	rent					
·		V <sub>CC</sub> = 5.5V				
loz	Hi-Z State	CE = High, V <sub>O</sub> = 0.5V		1	-40	μΑ
102		CE = High, V <sub>O</sub> = 5.5V	1		+40	μΑ
los	Short circuit <sup>4</sup>	CE = Low, V <sub>O</sub> = 0V	-15		-85	mA
Supply cur	rent					
Icc		CE = High, V <sub>CC</sub> = 5.5V		130	185	mA
Capacitano	ce <sup>7</sup>					
		CE = High, V <sub>CC</sub> = 5.0V				
CIN	Input	V <sub>1</sub> = 2.0V	1	5	10	pF
C <sub>OUT</sub>	Output	V <sub>O</sub> = 2.0V	1	8	13	pF

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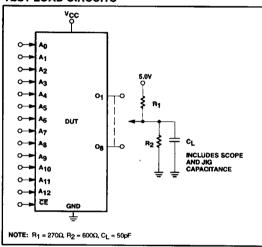
## AC ELECTRICAL CHARACTERISTICS -55°C ≤ T<sub>A</sub> ≤ +125°C, 4.5V ≤ V<sub>CC</sub> ≤ 5.5V

SYMBOL	PARAMETER	то	FROM	82HS641A		82HS641B			UNIT	
				Min	Typ <sup>5</sup>	Max	Min	Typ <sup>5</sup>	Max	
t <sub>AA</sub>	Access time <sup>6</sup>	Output	Address		40	55		40	45	ns
t <sub>CE</sub>	Access time <sup>6</sup>	Output	Chip enable		25	35		25	25	ns
t <sub>CD</sub>	Disable time	Output	Chip disable		25	35		25	25	ns

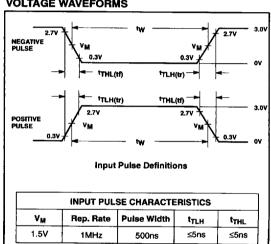
#### NOTES:

- Positive current is defined as into the terminal referenced.
- 2. All voltages with respect to network ground.
- 3. Measured with one output switching from a Logic "1" to a Logic "0".
- 4. Duration of short circuit should not exceed 1 second, no more than one output shorted at a time.
- 5. Typical values are at  $V_{CC} = 5V$ ,  $T_A = 25$ °C.
- Tested at an address cycle time of 1μs.
- 7. Guaranteed, but not tested.

### **TEST LOAD CIRCUITS**

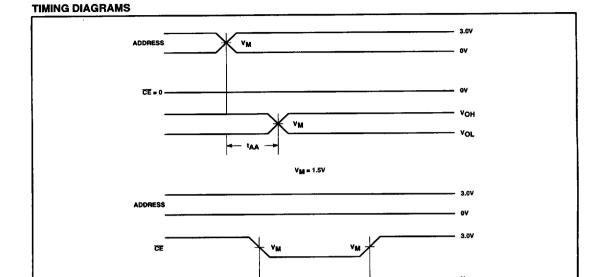


## **VOLTAGE WAVEFORMS**



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V<sub>M</sub> = 1.5V

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